

FIG. 1

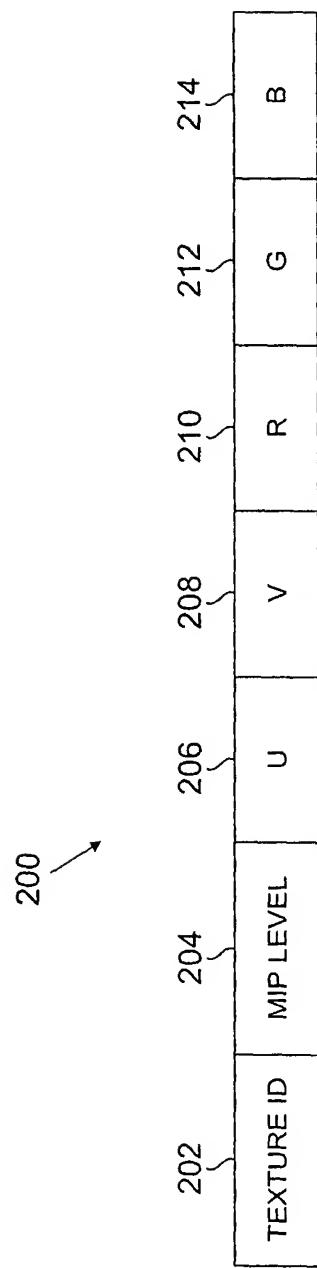
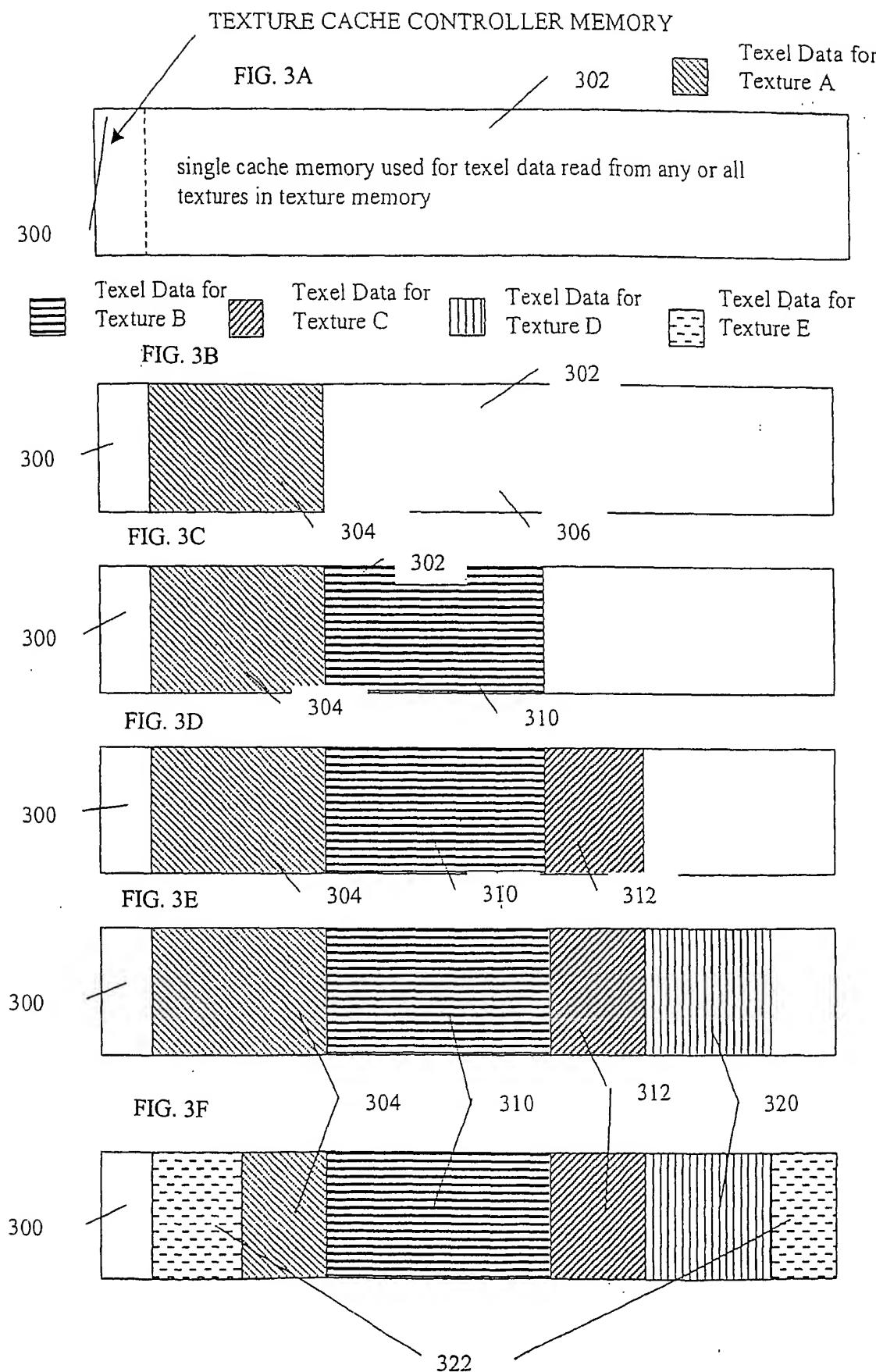


FIG. 2



## TEXTURE CACHE CONTROLLER MEMORY

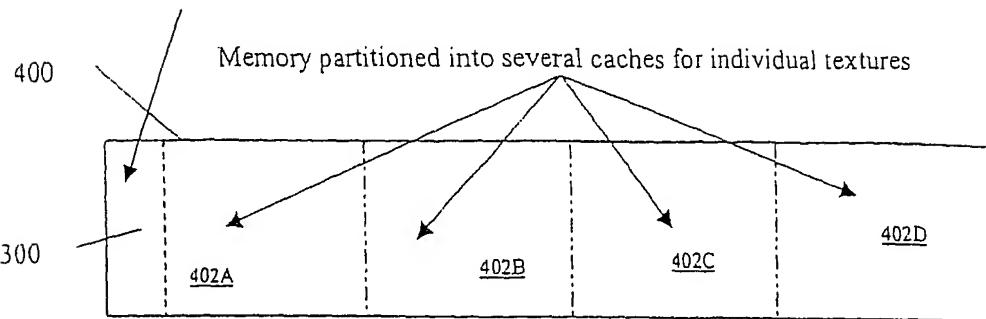


FIG. 4A

Texel Data for Texture A	Texel Data for Texture B	Texel Data for Texture C
Texel Data for Texture D	Texel Data for Texture E	Texel Data for Texture F

The width of the shaded area in each individual cache reflects the amount of texel data that may be used and re-used

FIG. 4B

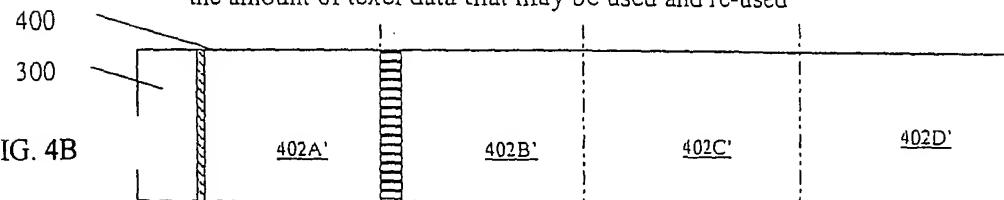


FIG. 4C

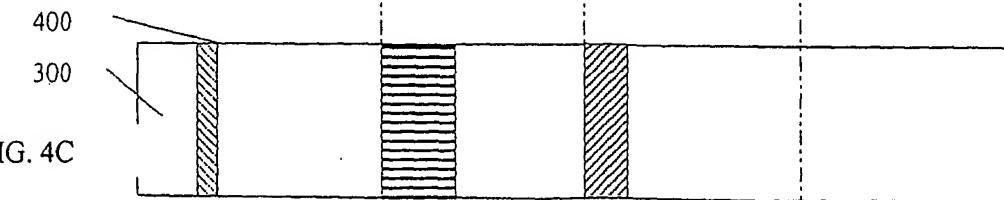


FIG. 4D

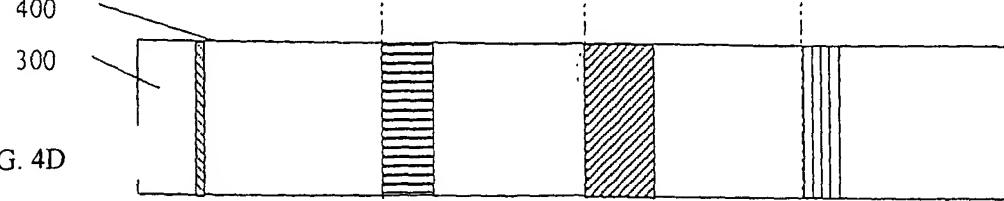


FIG. 4E

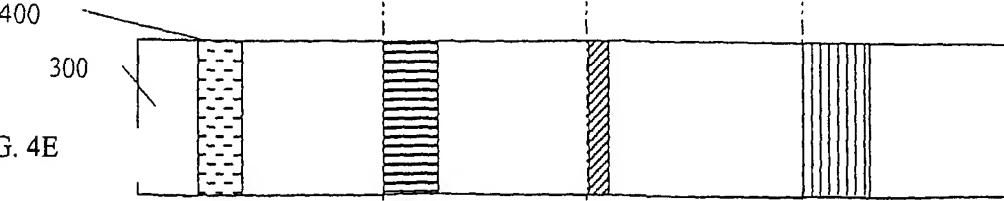
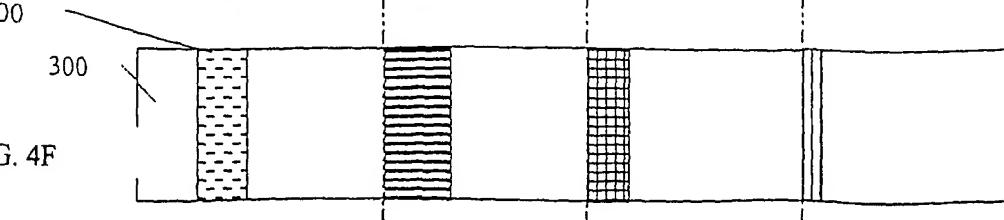


FIG. 4F



501A	502	504	506	508	510
TEXTURE ID	LOCATION OF THE ICA 402 IN THE CACHE 166	SIZE OF THE ICA	TEXTURE USAGE INDICATOR	CLOSED FLAG	
<u>501B</u>					
<u>501C</u>					
<u>501D</u>					
<u>501E</u>					
<u>501F</u>					

500

FIG. 5